

A B S T R A C T

A simulator fitted with at least one microprocessor sends input simulation signals to a unit under test, and receives therefrom output signals in reaction thereto. The method consists in processing some of the output signals from the unit as they are issued by means of a programmable logic circuit, in storing parameter values corresponding to said processed signals, and in giving the microprocessor access to the stored parameter values at a frequency which is compatible with its own operating speed. The apparatus enables the method to be implemented. The simulator comprises at least one programmable logic circuit, e.g. of the FPGA type, that is suitable for receiving at least some of the signals output by the electronic unit.